

#6/A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Atty. Docket

MARCUS KUEGLER ET AL

CH 000007

Serial No.

Filed: CONCURRENTLY

A METHOD AND APPARATUS FOR TESTING DIGITAL CIRCUITRY

Honorable Commissioner of Patents and Trademarks  
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Sir:

Prior to calculation of the filing fee and examination, please  
amend the above-identified application as follows:

IN THE CLAIMS

Please amend the claims as follows:

B  
at  
4. (Amended) A method as claimed in Claims 1, whilst in connection  
with said buffering executing a conversion between a digital full  
swing internal signal and an analog low swing external signal with  
respect to core circuitry of said digital circuitry.

5. (Amended) A method as claimed in Claims 1, whilst controlling  
both said loop-back as well as said buffering through a one-bit  
control signal.